

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE



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POSITRON 2K14



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In recent years, technology has made unprecedented growth around the world. Researchers, academicians, industries and the society at large have to work in union to meet the challenges of the rapidly growing discipline. The research organizations and industries are in need of highly skilled and scientifically oriented manpower. This manpower can be available only with flexible, adaptive and progressive training programs and a cohesive interaction among the research organizations, academicians and industries. Department of ECE, MITS has been consistently working towards the development of such manpower. Along with the host activities, student centered activities are also carried out in the department under MITSEC Association in order to achieve the above said goal. The faculties play their role in helping the students to timely upgrade their knowledge through the curriculum adapting innovative teaching techniques. We the students understand that the amount of time we spend on each of these activities is to lay a very strong platform to face the challenges of our future in the field of technology. The release of this edition of POSITRON 2k14 is an attempt to show the glimpse of departmental activities undertaken during the academic year 2013-14. Through this Newsletter we proudly present the accomplishments of our dedicated faculty and the sincere and smart working students in various technical fests. We are proud to publish the list of students who got placed in different multinational companies. The typical batches in the history of MITS, ECE (IV, III, II year students) worked well for this newsletter. We, the students of ECE department hope to see ourselves in the greatest heights of various Technical areas in the near future.

Message from the Correspondent:



Everyone should be glad to ensure POSITRON 2K14 as an excellent opportunity to make ourselves of being informative and knowledge oriented. I believe in the department of ECE will continue to work in fascinating this magazine an incredible one that looks forward advanced technology and with fully loaded commitment. Brace up yourself with the devices which in sense you with healthy valuable returns. Warm up with ultra-gaming experiences and find out your career option. I contemplate; this magazine will fasten your growth, development and

discriminate your knowledge at various levels. Grab the opportunities to the best of its exposure and illuminate your carrier, overcoming the odds against your efforts and endeavor. I am happy that every year the Department of ECE resolves to create a Magazine which cherishes the student's awareness in substantial making of his carrier.

- Sri. N Vijayabhaskar Choudary

Message from the Chairman:



With the philosophy of taking our students to the forefront of the new economy and to drive them by intellect and values, the enthusiastic Faculty drawn up this Magazine to their appreciation. It is for sure that this magazine will create real talk amongst the students and give access to the best of electronics and electrical engagements. To continue the challenging legacy of achieving excellence, I would therefore advice the students to be cognizant to multiply the knowledge. Know about the Faculty, polish yourself with their

help and set your minds to get holistic success.

- Sri. N Krishna Kumar

Message from the Principal:



To excel in technology that has been seeing new profiles for new generations. The technological information dissemination to public is the key factor in bringing concerned people/department together. The department of Electronics and Communication Engineering contributing best of its efforts in development of technical temper by publishing newsletter “Positron”. The documentation of different activities and bringing it to relevant technical community is the excellent towards service of society. These activities will help in making the science and

technology much stronger towards knowledge bank. I am congratulating all the ECE department staff and students on this occasion.

- Dr. K Sreenivasa Reddy

Message from the HOD:



I am happy to see one more issue of POSITRON - a departmental newsletter exclusively for students and faculty. Our faculty has achieved a distinct progress in teaching to the students, and conducting research in various fields of electronics and communication engineering. This newsletter brings all about their achievements. Our students have made deep impact in the Rayalaseema region by bagging several prizes in various events conducted by Engineering Colleges. Congratulations to all the winners. Exciting developments are taking place in the area of electronics and communication. The number of

transistors on a silicon die is ever increasing with the shrinkage of device geometry. Device sizes, as small as 22 nm, are being produced. A vertical layer is created and transistors are stacked like a “*double-decker*” bus to increase the transistor density. System-on-chip is accelerating the integration of the number of functions packaged in a module. The country is wired with fiber optic cables for backbone communication network. This network supplemented with the wireless networks is connecting India for voice, video and Internet services. Lot of research is carried out to realize long range and large bandwidth cellular communication networks. These objectives are achieved with the deployment Multi Input Multi Output (MIMO) antenna systems, and Orthogonal Frequency Division Multiplexing (OFDM) modulation. Research for developing 5G cellular products is in full swing and these products will be ready by year 2020. The 5G networks will provide 1 GB/s data rates under mobile conditions. The size of the Base Transceiver Station will be the same as that of small *match-stick* box. Opportunities are created with ITIR for Electronic Hardware Manufacturing (EHM) hubs. Investments will be made in VLSI, Embedded systems and other allied electronic products. It is up to the young engineer to grab the opportunity and make India rich. I wish success for all out going students of batch 2010-14.

- Dr. A R Reddy

ECE DEPARTMENT FACULTY:

Professors:



Dr. A R Reddy



Dr. S A K Jilani



Dr. D Asha Devi



Dr. J L M Iqbal

Associate professors:



Mr Mahesh



Mr M Jagadeesh Babu



Mr B Sukumar



Mr V Sai Kumar

Assistant professors:



Mr. M Sreenath Reddy



Mr. S Arun



Ms. C K Hemantha
lakshmi



Ms. G R Hemantha



Mr. J T Pramod



Mr. P R Ratna Raju K



Mr. M Venkata Srinu



Mr. R Ravindraiah



Mr. L Ashok



Mr. P Sravan Kumar



Ms. G Naga Swetha



Mr. V Sathish Kumar



Mr. D Balakrishna
Reddy



Mr. B Vamsi Krishna



Mr. U sreenivasulu



Mr. D Girish Kumar



Mr. G SambaSiva Rao



Ms. Naga Mounika

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Ms. N Hima Bindu



Ms. K Keerthi



Ms. L P Divya
Meenakshi

Teaching Assistants:



Ms. A Revathi



Ms. K Shobha Rani

Technical Staff:



Mr. P Md Akram



Ms. T Neeraja



Ms. M Manjula



Ms. Reddy Rani

Supporting Staff of ECE Department:



Mr. C Sreenivasulu



Ms. D Sasi Kala

DEPARTMENTAL ACTIVITIES:

PROFILE:

ECE department has a current intake of 240 UG students, 36 PG students for the specializations Digital Electronics & Communication Systems and Micro & Nano Electronics and 60 Diploma Students. Department has permanent affiliation to JNTUA, Anantapur and recently got accredited by NBA. It has all necessary laboratories required to conduct JNTUA curriculum. Department has all the facilities necessary to conduct mini-projects and main projects by II, III & IV year students. Students take up innovative projects in the area of Embedded Systems, VLSI, Signal Processing, Image processing, Voice processing, Robotics, Control Systems, Wireless Systems, etc. It is regular feature to win awards by our students at various forums such as Seminars, Symposiums, Conferences, and other events for project exhibitions, research paper presentations, script writing, sports, etc. The Faculty of ECE department is highly qualified with minimum of M.Tech degree qualification and impart high quality education to the students. Students are well placed in several companies across the world.

NEW RESEARCH INITIATIVES:

Electronics and Communication Engineering department is conducting research in the following areas:

1. Signal and image processing
2. VLSI design
3. Embedded Systems
4. Wireless communication systems

The research in the above four areas is conducted by Dr SAK Jilani, Dr M Iqbal, Dr D Asha Devi and Dr A R Reddy, respectively. These are assisted by remaining faculty members. The department is publishing 50 research papers per year.

ECE department has submitted 4 major projects by Professors and 8 minor projects by Assistant Professors to UGC for Rs. 1 Cr in year 2013-14.

The above achievements are made possible due to the following initiatives:

1. Research work by UG students: Under Graduate students are conducting research by taking up a mini project and completing from second year onwards. This is helping the students to design and develop electronic products for communication systems. Students are able to demonstrate the same by presenting research papers at various student's symposia or conferences.
2. Research by PG students: ECE department has students in two streams: 1) Digital Electronics and Communication Systems, and 2) Micro & Nano Electronics. Students of both the streams are conducting research in their core areas. Each

student is carrying out the PG project for one year and publishing two research papers. One research at the middle of the project on literature survey and another at the end of project on experimental results. Student publications and thesis is not exceeding 30% of similarity index of plagiarism.

3. Research by Faculty: Each faculty is deputed to workshops and conferences for acquiring the state-of-the-art developments in their research domains. Each faculty is publishing a minimum of two research papers per year.
4. Infrastructure: ECE department has infrastructure to conduct the research in the above four core areas.
5. Research Center: ECE department is recognized research center by JNTUA, Anathapur.
6. Research Scholars: ECE department is expecting full time research scholars under TEQIP. The research scholar can register with JNTUA, Anathapur for Ph.D. program.

ILLUMINATUS:

The word ILLUMINATUS in Latin means “To give light”, it is an ancient underground society by some eminent scientists in the early part of the 11th century A.D. The group of scientists had to carry out research work secretly as the church was very powerful at the time and shunned scientific work, which they considered to be the pagan. Illuminatus is student’s voluntary organization of Electronics and Communication Engineering branch. It was started in 2005 by small group with a burning desire to improve. The volunteers of the organizations are called ILLUMINATI.

We named our students association as ILLUMINATUS, keeping in mind the very fact that modern technical education provides no scope for overall personally development. The main motives behind forming such an association are to expose the students to various aspects of learning. The association meets once in a week within allotted hour. The various programs covered in the association meetings are Seminars, Paper Presentations, Guest lectures, G.Ds, Quizzes and other innovative ideas that not only relax the students from mentally toxing heavy academic work but also teach the students some new skill.

It provides the platform for the students to exchange their innovative ideas to expose their inner talents. We released a logo for the Quiz and conducted intra Departmental Quiz program and paper presentations. To encourage the students and for their active participation, illuminates award prizes for the winners. The students discuss about advanced technology so that it provides flexibility to switch the modern technology and practical knowledge.

The important works carried under this are:

Seminars, Paper Presentations, Debates, Group discussions, jam, Quiz and more...!

The above mentioned activities make the students overcome their stage fear apart from gaining knowledge.

Some pictures of the recently held Illuminatus program are displayed below. Sunil Das G, Consultant at Mindtree Ltd., Bangalore from 2002 ECE batch, was invited as guest for this program.





DETAILS OF THE ECE FACULTY PUBLICATIONS IN 2013-14

DR. A R REDDY:

Dr A R Reddy, the Head of the Department of Electronics and Communication Engineering department, had completed his Ph.D. on Micro-electronics (SAW devices) from IIT Kharagpur, 1986. He had 28 years of industry and teaching experience and he is the HOD of MITS-ECE since 4 years. He attended 24 National Conferences and 14 International Conferences. He published 5 National Journals and 12 International journals as well as 22 workshops attended (internal and external).

ACHIEVEMENTS OF DR. A R REDDY:

NATIONAL JOURNALS:

1. A R Reddy and S K Lahiri, Scattering parameters of interdigital and group type unidirectional SAW transducers, Journal of IETE, Vol.31, pp46-51, 1985
2. A R Reddy and S K Lahiri, Frequency tuning of low loss SAW delay line oscillator, IETE Technical Review, Vol.4, No.1, pp20-26, 1987
3. A R Reddy and S K Lahiri, Surface acoustic wave unidirectional transducers – a brief review, IETE Technical Review, Vol. 4, No.2, p41-51, 1987.
4. A R Reddy and S K Lahiri, Surface acoustic wave reflection from metal-strips and transducers, Journal of IETE, Vol.33, No.2, p57-62, 1987.
5. A R Reddy, A Review of digital Certificates, IETE Golden Jubilee Compendium: Evolution and perspective – Electronics, Telecommunications, IT and Broadcasting, pp.97-105, September 2003.

INTERNATIONAL JOURNALS:

6. A R Reddy and S K Lahiri, On window functions, International Journal on Electronics, Vol.56, pp809-813, 1984.
7. A R Reddy and S K Lahiri, SAW Injection Locking oscillator using built-in injector, International Journal on Electronics, Vol.62, No.2, pp251-256, 1987.
8. A R Reddy, Design of SAW band pass filters using new window functions, IEEE transactions on Ultrasonic, Ferroelectrics, and Frequency Control, Vol.UFFC-34, No.1, pp50-56, 1988.
9. Mohan H.S and A. R. Reddy, "An Effective Defense Against Distributed Denial of Service in Grid", "IEEE International conference on integrated intelligent computing ICIIC-2010, Aug 5-7, 2010, SJBIT, Bangalore. ISBN 978-0-7695-4152-5, PP 84-89, Published in IEEE Explore.
10. Mohan H.S, and A. R. Reddy, Generating the New S-box and Analyzing the Diffusion Strength to Improve the Security of AES Algorithm, IJCNS September issue, pp.51-56, Vol.2, No.9, 2010.
11. R. Elumalai and A R Reddy, Improving diffusion power of AES Rijndael with 8x8 MDS, International Journal of Scientific and Engineering Research (IJSER), pp.1-6, Vol.2, Issue 3, March, 2011. R. Elumalai and A R Reddy, Optimized implementation of Rijndael on controller R8C, International Journal of Scientific and Engineering Research (IJSER), pp.1-5, Vol.2, Issue 2, February, 2011. Mohan H.S and A. Raji Reddy. "Performance Analysis of AES and MARS Encryption Algorithms", "International Journal of Computer Science Issues(IJCSI), pp.363-368, Vol. 8, Issue 4, No 1, July 2011 ISSN (Online): 1694-0814, (With impact factor of 0.242).
12. Mohan H.S and A. Raji Reddy. "Improving the Diffusion Power of AES Rijndael with Key Multiplication", "International Journal of Computer Applications (IJCA), pp. 39-44,

Vol. 30, No. 5, September 2011, ISSN(online):0975-8887 ISBN: 978-93-80864-88-8, (with impact factor of 0.835).

13. Mohan H.S and A. Raji Reddy. "Securing the Multilevel Information System", "International Journal of Computer Science and Information Security (IJCSIS),pp. 29-35, Vol. 9, and No.9 September 2011. ISSN (online): 1947 5500, (with impact factor of 0.423). This paper is published as "best paper" category.
14. Mohan H.S and A. Raji Reddy. "Revised AES And Its Modes of Operation", "International Journal of Information Technology & Knowledge Management (IJITKM), Vol. 5, No.1, Nov 2011 (ISSN: 0973-4414)(with impact factor of 0.475) .

NATIONAL CONFERENCES:

15. A R Reddy and S K Lahiri, SAW oscillator using low loss delay line, IETE Symposium on Microwave Communication, April 8-9, 1983, Calcutta.
16. A R Reddy and S K Lahiri, In(cosx) window functions for FIR filters, National Conference on Digital Signal Processing, April 9-10, 1984, Burla.
17. A R Reddy and S K Lahiri, Analytical design of FIR filter using optimum window functions, Symposium on Modern Trends in Electronic Instrumentation and Digital Communication Systems, `August 13-14, 1984, Warangle.
18. A R Reddy and S K Lahiri, High performance SAW injection locking oscillator using low loss UDTs, All India Symposium on Communication, August 24-25, 1984, Bangalore.
19. A R Reddy and S K Lahiri, A new SAW unidirectional transducer, National Acoustic Convention, December 17-18, 1984, Kharagpur.
20. A R Reddy, R Ramabrahmam and S K Lahiri, Slanted ground electrode SAW UDT, National Conference on Integrated Circuits, Communication and Signal Processing, December 17-18, 1984, Hyderabad.
21. A R Reddy, Secure file transaction, IETE Midterm Symposium, April, 2002, New Delhi.
22. A R Reddy, E-SEAL: A Certifying Authority software module for private entities, National Seminar on Networking and E-Security, January 17-18, 2003, Allahabad.
23. Pushpavati R, Yamuna Selvi, AR Reddy and Premananda BS, Integer arithmetic using SCICOS, National Conference on current trends in advanced computing and e-learning, 6-7 February 2009, Baramati, Maharashtra.
24. A Purushotham Reddy, AR Reddy, R Elumalai, Design , development and implementation of rate-monotonic scheduler in micro-kernel operating system, NCISE-10, 2nd National Conference on Information and Software Engineering, 26-27 February 2010, Chennai.
25. A Purushotham Reddy, AR Reddy, R Elumalai, Implementation of RM scheduler in μ os ii RTOS, 2nd National Conference on Signal processing, Communicaitons and VLSI design, 7-8 May 2010, Anna University, Coimbatore, Tamil Nadu.
26. Mohan H.S and A. R. Reddy, "A Survey of Intrusion Detection Systems", " 20th CSI Karnataka student convention", Oct 13-14, 2006, R.V.C.E. Bangalore.
27. Mohan H.S and A. R. Reddy, "An Approach for Certifying Security in Software Components", "National Conference on Emerging technologies and Trends in IT 2007", April 6-7,2007, New Delhi.
28. Mohan H.S, and A. R. Reddy, "A Noble Approach for Mosaicing of microscopic images based on pattern matching", "National Conference on Recent trends in Electronics and Communication", S.J.B.I.T., May 4, 2007, Bangalore.
29. Mohan H.S and A. R. Reddy, "A Candidate Cipher for Advanced Encryption Algorithm and Its Diffusion Strength", "National Conference on Recent trends in Information Technology", S.J.B.I.T., May 12, 2007, Bangalore.

30. Mohan H.S. and A. R. Reddy, "Face Recognition using Gabor Wavelet Networks", "National Conference on Recent trends in Electronics and communication, NCRTEC-2008", SJBIT , March 26, 2008, Bangalore.
31. Mohan H.S and A. R. Reddy, "Feature-Based Image Metamorphosis", "National Conference on Advanced trends in Information Technology", S.J.B.I.T., March 26, 2008, Bangalore.
32. Mohan H.S and A. R. Reddy, "STEGANOGRAPHY IN VIDEO", "2nd National Conference on Advances in Information Technology", S.J.B.I.T, April 4, 2009, Bangalore.
33. Mohan H.S and A. R. Reddy "Evaluating the performance of Various Architectures for Wireless Ad Hoc Networks", "04th NATIONAL CONFERENCE ON "COMPUTING FOR NATION DEVELOPMENT " INDIACOM , Feb 25-26, 2010", New Delhi.
34. Mohan H.S and A. R. Reddy, "Virtual network for the substrate network using an Efficient Embedding Algorithm", "NATIONAL CONFERENCE ON "ADVANCED COMPUTING & WIRELESS NETWORKS- ACWIN10", April 29-30, 2010, HKBK BANGALORE.
35. Mohan H.S and A. R. Reddy, "An Approach for Certifying Security in Software Components", "NATIONAL CONFERENCE ON ACADEMIC RESEARCH", Aug13-14th, 2010, Dr MGR University, Chennai. ISBN 978-81-910827-0-8.
36. S Lokesh and AR Reddy, Vehicle navigation system using ARM9 processor, National Conference on electronic communication systems, 16 March 2011, MITS, Madanapalle.
37. Y Pavan Kumar Reddy and AR Reddy, Zigbee and GPS based tracking system using ARM9, National Conference on electronic communication systems, 16 March 2011, MITS, Madanapalle.

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38. A R Reddy, B Majumdar and S K Lahiri, Group type quadrature phase unidirectional transducers for SAW filters, International Conference on Circuits and Communication Systems, December 25-28, 1981, Calcutta.
39. A R Reddy and S K Lahiri, SAW low loss filter design using optimum window functions, International Conference on Computer, Systems and Signal processing, December 10-12, 1984, Bangalore.
40. A.R. Reddy, and S.K. Lahiri, Scattering Parameters of Electrically Loaded S.A.W. Group Type Unidirectional Transducers, International Frequency Control Symposium Proceedings, IEEE UFFC, 1985.
41. A R Reddy and R Chattopadhyaya, Matching circuits for SAW band pass filters, International Symposium on Electronic Devices, Circuits and Systems, December 16-18, 1987, Kharagpur.
42. A R Reddy and R Chattopadhyaya, An active balun for MMICs, Asia Pacific Microwave Conference, October 26-28, 1988, China.
43. A R Reddy and R Chattopadhyaya, MMIC BPSK direct modulator at microwave frequency, European Microwave Conference, 1989, London.
44. Mohan H.S and A. R. Reddy, "Diffusion Analysis of Mars Encryption Algorithm", "International conference on current trends of information technology", Oct 1-3, 2005 Bhimavaram, Andhra Pradesh.
45. Mohan H.S and A. R. Reddy, "Mars Encryption Algorithm and its Diffusion Strength", "International Conference on EAIT", Feb 10-11, 2006, Kolkata.
46. Mohan H.S and A.R. Reddy, "Diffusion Analysis of a candidate cipher of Advanced Encryption Algorithm", "IEEE International Advance Computing Conference IACC-2009", March 6-7, 2009, Patiala-India.

47. Mohan H.S and A. R. Reddy, "An Effective Defense Against Distributed Denial of Service in Grid", "*First International conference on integrated intelligent computing*" ICIIC-2010, SJBIT, Aug 5-7, 2010, Bangalore. Published in IEEE Explore.
48. Mohan H.S and A. R. Reddy, "+-Generating the New S-box and analyzing the Diffusion Strength to Improve the Security of Rijndael algorithm", "*International conference on Computer Science and information Technology*", ICCSIT-2010, Sep 17-18, 2010, RLJIT, Doddaballpur.
49. A.Purushotham Reddy, AR Reddy, R Elumalai, "MUCOS RTOS for embedded systems" *International Conference on Communication, Computation, Control and Nanotechnology (ICN- 2010)* to be held at REC Bhalki, Karnataka, India, October 29-30, 2010.

DR S A K JILANI:

Dr S A K Jilani had pursued his Ph.D. on electronics from SK University, 2002. He joined MITS as professor in Electronics and Communication engineering department in 2004. He has got 13 years of teaching experience and is a professor of MITS-ECE since 10 years. He attended 2 National Conferences and 2 International Conferences. He published 5 National Journals and 15 International journals and attended 7 workshops (internal and external).

ACHIEVEMENTS OF DR S A K JILANI:

1. English Vowel Recognition using Area Function Approximation by Linear Predictive Analysis, ISSN 2249-0000 Volume 1, Number 2 (2011), pp. 103-110, Global Journal of Computational Intelligence Research. © Research India Publications
2. S A K Jilani, T. Syed Akheel, K. Kanthamma and, S. Javeed Hussain and Vekata Narasimhulu "An Efficient Face Recognition Method with FSVDRAND and RBF Neural Network", *International Journal of Engineering Research and Industrial Applications* (Vol 4 No III August 2011) pages 29-44.
3. S A K Jilani, T. Syed Akheel, and G.N.S Vaibhav, "A Modified Approach for Face Recognition Using Eigen Values", *International Journal of Math.Science & Engineering Application*, (Vol 5 No. IV, July 2011) Pages 241-251.
4. S A K Jilani, N.Venkatanath, "Image Processing Based Automatic Mesh Quality Analyser", *International Journal of Computer Applications in Engineering Sciences*, (Vol 1, Issue 11, June 2011) Pages 118-121.
5. S A K Jilani ,T. Syed Akheel, K. Kanthamma and D. Ramana Naik, " Comparative Study of Face Representation Methods for Efficient Face Recognition Using Singular Features" *International Engineering Research and Industrial Applications* (Vol 4 No II May 2011) pages 249-266.
6. S A K Jilani, T. Syed Akheel, K. Kanthamma and, S. Javeed Hussain "Comparative Study of Face Representation Methods for Efficient Face Recognition- Survey" *International Engineering Research and Industrial Applications* (Vol 4 No II May 2011) pages 287-304.
7. S A K Jilani ,T. Syed Akheel, and K. Kanthamma, " Different Methods for Statistical Face Representation – Review" , *Oriental Journal of Computer Science & Technology*(Vol.4(1) Pages 11-28
8. Dr. S.A.K. Jilani, G.N. Kodandaramaiah, M.B. Manjunatha, M.N. Giriprasad, R.B. Kulkarni & M. Mukunda Rao" The Minimal and Maximal Vocal Tract Shape Variability for Vowels Based on LPC", *International Journal of Highly Reliable*

Electronic Systems (Vol.3 No.1 Jan-June 2010) Pages: 39-45,
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10. Dr.S.A.K.Jilani, T.Syed Akheel, K.Kanthamma, C. Sreevardhan, "Brain Image Segmentation Using RBF Neural Network", Proceedings of international conference on MEMS & Optoelectronics Technologies (ICMOT 2010), 22-23 January 2010, Pages 361-364
11. Dr.S.A.K.Jilani, T.Syed Akheel, K.Kanthamma," A Location Based Distributed Database Architecture For Global Roaming in Next Generation Mobile Networks" Proceedings of international conference on MEMS & Optoelectronics Technologies (ICMOT 2010), 22-23 January 2010, Pages 331-334
12. S.A.K Jilani, Abdul Muhith & D. Karuna Sagar, "Out-of-focus point spread functions of optical systems in the presence of primary spherical aberration and astigmatism", Journal of Pure and Applied Physics Vol. 21, No. 4, Oct. – Dec., 2009, pp. 633 – 637
<http://jpaph.com/Vol21IV.aspx#13>
13. S.A.K Jilani, R. Subrahmanyam, "Temperature Coefficients of Permeability and Induced EMF in Ferromagnetic Material", Journal of Pure and Applied Physics, Volume 21 - II (Apr - Jun, 2009)
<http://jpaph.com/Vol21II.aspx#17>
14. Jilani, S.A.K, Kodandaramaiah, G.N. Manjunatha, M.B. "Use of lip synchronization by hearing impaired using digital image processing for enhanced perception of speech", IEEE Transactions, Computer, Control and Communication, 2009. IC4 2009. 2nd International Conference on Digital Object Identifier, Page(s):1 – 7, Feb. 2009.
15. S.A.K Jilani, T. Chandrasekhar, Ch.V.V. Ramana1, C. Mani Kumar, K. Nagabhushan Raju , "Design and Development of Colour Identification System Using AT89C51 Microcontroller", International Journal of Materials Sciences ISSN 0973-4589 Volume 3, Number 3 (2008), pp. 223–228 Research India Publications
<http://www.ripublication.com/ijoms.htm>
16. Dr.S.A.K.Jilani, and G Rajashekar, "Microcontroller 89C51 Based Universal Data Converter", Lab Experiments, pp 184-194, Sept 2008.
17. Dr.S.A.K.Jilani, P.Mohan Kumar, and B Sukumar, "Determination of Hidden Surface Of A Regular Cylindrical Object", Lab Experiments, pp 317-322, Dec 2006.
18. Dr.S.A.K.Jilani , G.Satheesh Babu and Dr.K.Malakondaiah, "Interfacing of Simple Experiments with PC LPT Parallel Port", Lab Experiments, pp 218-227, Sept-2005.
19. Dr S.A.K.Jilani, T.Chandra Shekar, Dr K.Nagabhushan Raju, "PC Based Sound Recording and Reproduction", Lab Experiments, pp 199-205, Sept -2005.
20. Dr S.A.K.Jilani, T. Chandra Shekar, G.Satheesh Babu, Dr K.Nagabhushan Raju, "Micro controller based BPL TV Remote Decoder for Home Appliance Control", Lab Experiments, pp 113-118 ,June-2005.
21. Dr.S.A.K.Jilani, G.Satheesh Babu, and Dr.K.Malakondaiah, "Interfacing of Matrix Keyboard with Microcontroller", Lab Experiments, pp 254-262, Dec-2004.
22. Dr S.A.K Jilani, Nagabhushana katte, Dr Nagabhushan Raju, G.Satheesh Babu, "Microcontroller Based Temperature Controller", Lab Experiments, pp 101-110 June-2004.
23. Dr S.A.K.Jilani, Dr K.Nagabhushan Raju, G.Satheesh Babu, " Universal Data Converter", Lab Experiments, pp 41-45, March-2004.
24. Dr. S.A.K.Jilani ,M.B.Manjunatha, G.N.Kodandaramaiah ,Dr.M.N.Giriprasad, Prof.R.B.Kulkarni , and Dr.M.Mukundarao ."Analysis of Lip Synchronization using

- Image Processing, International Proceedings of World Academy of Science, Engineering and Technology, International Conference on Image and Vision Computing, 2008, Bangkok, Thailand, Volume 35, October 2008.
25. Dr. S.A.K.Jilani, G.N.Kodandaramaiah, M.B.Manjunatha, Dr.M.N.Giriprasad, Prof.R.B.Kulkarni, and Dr.M.Mukundarao, "Use of Lip synchronization By hearing impaired using DIP for enhanced perception of speech", IEEE-IC4 2009, Karachi, Pakistan, Accepted for Inclusion in the IEEE Explorer.
 26. S A K Jilani, N.Venkatanath, "Image Processing Based Automatic Mesh Quality Analyser", International Journal of Computer Applications in Engineering Sciences", (Vol 1, Issue 11, June 2011) Pages 118-121.
 27. S A K Jilani, T. Syed Akheel, K. Kanthamma and D. Ramana Naik, "Comparative Study of Face Representation Methods for Efficient Face Recognition Using Singular Features" International Engineering Research and Industrial Applications (Vol 4 No II May 2011) pages 249-266.
 28. S A K Jilani, T. Syed Akheel, K. Kanthamma and, S. Javeed Hussain "Comparative Study of Face Representation Methods for Efficient Face Recognition- Survey" International Engineering Research and Industrial Applications (Vol 4 No II May 2011) pages 287-304.

DR D ASHA DEVI:

Dr D Asha Devi had pursued her Ph.D. on Embedded systems in 2012 from SK University. She joined MITS as professor in Electronics and Communication engineering department in 2012. She has got 12 years of teaching experience and is a professor of MITS-ECE since 2 years. She attended 18 National Conferences and 6 International Conferences. He published 1 National Journals and 8 International journals along with 6 workshops attended (internal and external).

ACHIEVEMENTS OF DR. D ASHA DEVI:

1. Embedded Based System for the Measurement of Conductivity and Monitoring Through Web I J E S C E © RESEARCH SCIENCE PRESS, Vol. 3, No. 2, July-December 2011.
2. Measurement of Potassium Levels in the Soil using Embedded System based Soil Analyzer International Journal Of Innovative Technology & Creative Engineering (Issn:2045-8711) Vol.1 No.1 January 2011
3. Design and Implementation of CPLD based Solar Power Saving System for Street Lights and Automatic Traffic Controller International Journal of Scientific and Research Publications, Volume 2, Issue 11, November 2012. ISSN 2250-3153
4. Information Delivery System through Bluetooth in Ubiquitous Networks Journal of Advanced Networking and Applications Vol. 01 No. 01 pages: 57-62 (2009)
5. Development of a Hierarchical K-Selecting Clustering Algorithm – Application to Allergy Published in International Journal of Advanced Engineering & Applications, Jan. 2010
6. Dr D Asha Devi attended International Conference on Competency Building Strategies in Business and Technology for Sustainable Development and Presented a paper titled "An authentication frame work for ubiquitous computing environment" Organized by Sri Ganesh School of Business Management, Attur Main Road, Mettupatti, Valapaddy (Tk), Salem-Dt, Tamil Nadu, INDIA held on 22 February 2013.
7. Dr D Asha Devi Presented a paper titled "A Study on Security challenges for

ubiquitous computing using role based access control system” in a National conference on Cryptography and network security" Organized by Department of CSE, SVS Group of Institutions, Bheemaram, Hanamkonda, on 22-03-2013 & 23-03-2013.

8. Dr D Asha Devi attended "AICTE Sponsored Faculty Development Program (FDP) on “VLSI DESIGN” organized by VNR Vignana Jyothi Institute of Engineering & Technology, Hyderabad from 20-05-2013 to 01-06-2013 (10AM to 5PM).About 80 participants were benefited by the training programme.
9. Dr D Asha Devi Appointed as Reviewer for INTERNATIONAL JOURNAL FOR ADVANCED ENGINEERING & GLOBAL TECHNOLOGY (IJAEGT).
10. Dr D Asha Devi Attended India’s Flagship Nanotech Event - 3 day conference on 6th Bangalore India Nano organized by the Department of IT, Biotechnology and Science & Technology, Government of Karnataka under the able guidance of the Vision Group on Nanotechnology from 4th to 6th December 2013.
11. Presented Paper entitled “A Study on Implementing Data Mining techniques to improve customer Behavior” at the first International Conference on Computers and computing – CMRTC – Hyderabad on 27th March 2014
12. Presented a Paper at UGC Sponsored National Seminar on "Sustainable Management of Natural Resources: Challenges and Responses-Organized by the Department of Rural Development & Social Work, Sri Krishnadevaraya University, Anantapuramu. The title of the paper is “A Study on the Agriculture Crisis - Ailments and remedies for sustainable development - with reference to Anantapuram District" on 24th & 25th March 2014.
13. Presented a paper titled “Resource Allocation and Scheduling in the Cloud Computing environment" in “UGC Sponsored National Conference on Recent Trends in web Sciences (NCRWS-2014) organized by the Department of Computer Science, Dravidian University, Kuppam on 14 & 15 March 2014.
14. Presented a paper entitled “ Food Security Bill- Boon or Bane” at UGC Sponsored National Seminar on “ Food Security Bill” – Organized by Department of Political Science & Public Administration, Sri Krishnadevaraya University, Anantapuramu on 7th & 8th March 2014.
15. Presented paper entitled “A Cloud based Security for Intelligent System" at 3rd National Conference on computer networks and information security -2014 Sponsored by AICTE and organized by Department of IT, Vasavi Engineering College, Hyderabad on 4th & 5th March 2014.
16. Presented a paper on “A Study on the Agricultural Development of Anantapur Anantha Water Grid (Anantha JalaValayam)” to the first two day national conference on sustainable water resource management (Solutions, technologies & opportunities) – organized by Department of Civil engineering, Maharaj vijayaram Gajapathi Raj College of engineering, Vijayanagaram sponsored by AICTE on 21st & 22nd February 2014.

DR. J.L MAZHER IQBAL:

Dr.J.L Mazher Iqbal had pursued his Ph.D. on Performance Improvement in Reconfigurable Computing for DSP from S V University College of Engineering, S V University. He joined MITS as professor in Electronics and Communication engineering department in 2013. He has got 14 years of experience and is a professor of MITS-ECE since one year. He attended 4 National Conferences and 2 International Conferences. He published two National Journals and four International journals and attended ten workshops (internal and external).

ACHIEVEMENTS OF DR. J.L MAZHER IQBAL:

1. Editorial board member - Integrated Optics and Light wave: An International Journal (OPTLJ) <http://airccj.org/science/optlj/editorial.html>.
2. Reviewer - Circuits System and Signal Processing, Springer Journal (Impact factor 0.982) , ISSN0278-081X(print), ISSN1531-5878 (electronic)
3. Reviewer – IET Signal Processing (Impact factor 0.561), ISSN1751-9675
4. Publicity Co-Chair in the International Conference CCPE-2011, conducted by ACEEE at pune, Proceedings is published in Springer LNCS-CCIS series
5. J.L Mazher Iqbal, S. Varadarajan, “High Performance Reconfigurable FIR Filter Architecture Using Optimized Multipliers”, Circuits System and Signal Processing, Springer Journal No.34, DOI 10.1007/s00034-012-9473-3, 2013:32, pp:663-682, 2013, Impact Factor 0.982, ISSN0278-081X(print), ISSN1531-5878 (electronic).
6. J.L Mazher Iqbal, S. Varadarajan, Memory Based and Memory Less Computation for Low-complexity Reconfigurable Digital FIR filter, WSEAS Transactions on Systems, Issue. 3, Vol. 12, pp:142-153, 2013, Print ISSN: 1109-2777, E-ISSN: 2224-2678.
7. J.L Mazher Iqbal, S. Varadarajan, “High Performance Reconfigurable Balanced Shared Memory Architecture For Embedded DSP”, International Journal of Computer Science and Information Security, IJCSIS, USA, Vol. 8, No. 4, pp:198-206, July 2010, ISSN 1947 5500.
8. J.L Mazher Iqbal, S. Varadarajan, “A New Algorithm for FIR Digital Filter Synthesis for a Set of Fixed Coefficients”, European Journal of Scientific Research, EJSR, Vol.59 No.1 (2011), pp.104-114, July 2011, ISSN 1450-216X.
9. J.L Mazher Iqbal, Dynamic Reconfigurable Architecture for AES Cryptosystem, Journal of Innovation in Electronics and Communication-Special Issue, vol.2, no.2, pp183-188, Jan 2012, ISSN 2249-9946.
10. J.L Mazher Iqbal et al, Error Correction in Insertion/Deletion Channels using LDPC Code, Journal of Innovation in Electronics and Communication-Special Issue, vol.2, no.2, pp248-251, Jan 2012, ISSN 2249-9946.
11. J.L Mazher Iqbal, S. Varadarajan, Performance Comparison of Reconfigurable Low Complexity FIR Filter Architectures, International Conference on Computational Intelligence and Information Technology, CIIT, Springer LNCS-CCIS, vol. 250, pp.637-642, Nov 2011, Springer-Verlag Berlin Heidelberg 2011, Springer Digital Library, ISSN 1865-0929, e-ISSN 1865-0937, ISBN 978-3-642-20572-9, e-ISBN 978-3-642-20573-6 DOI: 10.1007/978-3-642-25734-6_151.
12. J.L Mazher Iqbal, S. Varadarajan, Performance Improvement in Reconfigurable Computing Systems, International Conference on Sensors, Security, Software and Intelligent Systems (ISSIS 2009), Coimbatore Institute of Technology, Coimbatore and Oklahoma State University USA, pp. VLSI | 29-35, 8 - 10, January 2009.
13. J.L Mazher Iqbal et al, E- Payment System Using MICR- ICR For Automatic Bill Transaction, International Conference on Systemics, Cybernetics and Informatics (ICSCI 2005), Pentagram Research Center (P) Limited, Hyderabad, pp.120-125, January 2005.
14. J.L Mazher Iqbal et al, Verification of Parts Using Digital Image Processing in Machine Vision, National Conference on Signal and Image Processing (NCSIP-2012), Sri Sai Aditya Institute of Science and Technology, Surampalem, pp 175-180, 2012, ISBN 978-93-5067-470-3.
15. J.L Mazher Iqbal et al, Design Of Memory-Based FIR Filter Using Optimized Look-Up-Table, National Conference on Recent Trends in Information Technology (NCRTIT-12) , RMK Engineering College, Chennai, pp 32-39, 20, February 2012.

16. J.L Mazher Iqbal et al, Chaotic Map Based Image Encryption Algorithm Using S Box, National Conference on Wireless Information and Networking in Global Systems (WINGS2011), KLN College of Engineering, 2011.

MR MAHESH:

Mr Mahesh is pursuing his Ph.D. thesis on image processing from JNTU Anantapur College. He joined MITS as an associate professor in Electronics and Communication engineering department in 2007. He has got 12 years of teaching experience and is working as an associate professor in MITS-ECE since 7 years. He attended 3 National Conferences and 3 International Conferences. He published 9 International journals and attended 10 workshops (internal and external).

ACHIEVEMENTS OF MR MAHESH:

1. Mr. Mahesh Published a paper titled Feature Based Image Mosaic Using Steerable Filters and Harris Corner Detector in an International Journal of Image, Graphics and Signal Processing (IJIGSP) ISSN: 2074-9074(Print), ISSN: 2074-9082 (Online)" Pp-09-15, DOI: 10.5815/ijigsp, Volume 5, Number 6, May 2013 .
2. Mr. Mahesh Presented a paper titled "Automatic Image Mosaic system Using Steerable Harris corner Detector " in an international Conference on Machine Vision and image Processing (MVIP12), Organized by the Department of Instrumentation and Control Engineering, PSG College of Technology,Coimbatore-6411004, Tamil Nadu, India.
3. Published a paper titled "Corner Detection Enhancement Using Steerable Filters" in an international Journal of Scientific & Engineering Research Volume 3, Issue 8, ISSN 2229-5518,2012.
4. Published a paper on Signal & Image Processing : An International Journal (SIPIJ) Article: INVARIANT CORNER DETECTION USING STEERABLE FILTERS AND HARRIS ALGORITHM, Signal & Image Processing : An International Journal (SIPIJ), Vol.3, No.5, ISSN : 0976 - 710X (Online) ; 2229 - 3922 (print), October 2012, pp:111-118.
5. Presented a paper titled "Feature Based Image Registration Using Steerable Filters and Harris Algorithm" in Fourth International Conference on Advances in Recent Technologies in Communication and Computing (ARTCom 2012), Organized by the Association of Computer Electronics and Electrical Engineers (ACEEE).
6. An International FLI on "Image Processing", 05-07-2010 to 09-07-2010, MITS, Madanapalle.
7. A National Workshop on "Advanced Processors for Embedded Systems", 25-10-2010, MITS, Madanapalle.
8. A National Conference on "Electronic Communication Systems", 16-03-2011, MITS, Madanapalle.
9. An International FLI on "Embedded Computing system: From Processor Design to Code Optimization", 20-06-2011 to 24-06-2011, MITS, Madanapalle.
10. A National Workshop on "Digital Image Processing", 30-09-2011, MITS, Madanapalle.
11. Low-resolution satellite image enhancement using DT-CWT & SVD IJARECE, VOL 1,ISSUE 4,OCT 2012 ISSN: 2278-909X PP:40-45
12. Invariant Corner Detection Using Steerable Filters And Harris Algorithm Signal & Image Processing : An International Journal (SIPIJ) Vol.3, No.5, October 2012

13. Image Mosaic Using Speeded Up Robust Feature Detection International Journal of Advanced Research in Electronics and Communication Engineering (IJARECE) Volume 1, Issue 3, September 2012, ISSN: 2278 – 909X
14. Corner Detection Enhancement Using Steerable Filters International Journal of Scientific & Engineering Research Volume 3, Issue 8, August-2012
15. Sift Based Image Mosaic Algorithm International Journal of Communications and Engineering (IJCAE), IJCAE, Vol.3 Issue 1, July 2012, 9- 15, ISSN-0988-0382E
16. Image Mosaic Using FAST Corner Detection ISSN: 2278 – 909X International Journal of Advanced Research in Electronics and Communication Engineering (IJARECE) Volume 1, Issue 6, December 2012

MR M JAGADEESH BABU:

Mr M Jagadeesh Babu joined MITS in Electronics and Communication Engineering department in 2011. He completed his post-graduation from Anna University Specialized on 'Applied Electronics' in 2006. He has got 8 years of teaching experience and is working as an associate professor in MITS-ECE since 3 years. He attended 3 International Conferences and 1 National Conference. He published 4 International journals along with attended 4 workshops (internal and external).

ACHIEVEMENTS OF MR M JAGADEESH BABU:

1. Attended Two day workshop on "Xilinx dsp design flow and implementation of image and signal processing applications using xilinx FPGA's (sponsored by APSICHE) & Conducted by Department of Electronics & Communication engineering JNTUA college of Engineering (Autonomous) Ananthapuramu, Andhra Pradesh held during 24th -25th January 2014.
2. Attended India's Flagship Nanotech Event - 3 day conference on 6th Bangalore India Nano organized by the Department of IT, Biotechnology and Science & Technology, Government of Karnataka under the able guidance of the Vision Group on Nanotechnology from 4th to 6th December 2013.
3. Attended one Day IEEE Conference organizers workshop (13 October,2012) organized by IEEE Hyderabad Section and Cosponsored by IEEE Vizag Bay Subsection.
4. Attended a Two Day International Workshop on "GLOBALLY RELEVANTELECTRIAL AND COMPUTER TECHNOLOGY CURRICULUM" Through Indo-US Collaboration for Engineering Education August 16& 17, 2011 organized by MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE.
5. Attended One day National work shop on "Digital Image processing" organized by Dept of ECE, Madanapalle Institute of Technology on 30-09-2011.
6. Attended 2 days Technical workshop on RENEW-ROBOTICS organized by Vignana Bharathi Institute of Technology and conducted by BITS-PILANI on 27th - 28st Feb, 2010.
7. Participated and worked as the program co-ordinator for The MATERA-06 (5-days course on MATLAB) conducted by Dept, ECE, Vignana Bharathi Institute of Technology, Ghatkesar, Hyderabad.
8. Attended a 3-days workshop on "Linux "conducted by the Dept of CSE&IT, Vignana

Bharathi Institute of Technology, Ghatkesar, Hyderabad.

9. Attended a 2-day workshop on "Embedded Software" conducted by ECIT sponsored by CBIT on 7th & 8th, Sep 2007, Hyderabad.
10. Radio frequency grid for electronic voting machine theft prevention based on MEMS and GSM IJARECE, Vol 1, Issue 4, Oct 2012 ISSN: 2278-909X

MR. B SUKUMAR:

Mr B Sukumar joined MITS in Electronics and Communication Engineering department in 2006. He completed his post-graduation from J.N.T.U Specialized on 'DECS' in 2007. He has got 7 years of teaching experience and is working as an associate professor in MITS-ECE since 6 years. He attended 2 National Conferences. He published 3 National Journals and 2 International journals and attended 12 workshops (internal and external).

ACHIEVEMENTS OF MR. B SUKUMAR:

1. Mr. B. Sukumar attended Pedagogical training at Engineering Staff College of India (ESCI), Hyderabad during 20-01-2014 to 25-01-2014.
2. A National Workshop on "Advanced Processors for Embedded Systems", 25-10-2010, MITS, Madanapalle.
3. A National Conference on "Electronic Communication Systems", 16-03-2011, MITS, Madanapalle.
4. An International FLI on "Embedded Computing system: From Processor Design to Code Optimization", 20-06-2011 to 24-06-2011, MITS, Madanapalle.
5. A National Workshop on "Digital Image Processing", 30-09-2011, MITS, Madanapalle.
6. "Globally Relevant Electrical And Computer Technology Curriculum", 16-08-2011 to 17-08-2011, MITS, Madanapalle.
7. A Qualitative Approach for Denoising of CFA Images by PCA using Co-Variance method International Journal of Humanities, Education Technology and Management ISSN (Print):2319-1767 Vol.1, Issue 1 , August(2012)

MR SAI KUMAR V:

Mr Sai kumar V joined MITS in Electronics and Communication Engineering department in 2011. He completed his post-graduation from V.I.T Specialized on 'VLSI Design' in 2007. He has got 7 years of teaching experience and is working as an associate professor in MITS-ECE since 3 years. He attended 1 National Conference and 2 International Conferences. He published 1 national journal and attended 7 workshops (internal and external).

ACHIEVEMENTS OF MR SAI KUMAR

1. "Compensated Noise Reduction in B&W motion picture films.", Lab Experiments, pp 184-194, March 2011.
2. International Conference on Frontiers of Computer Science, 7th – 9th Aug 2011, Atria Institute of Technology, Bangalore.
3. Sai Kumar.V ., "Vehicle parking occupancy information system" National Level Conference on Electronic Communication Systems held on 16th March 2011 at Department of ECE, Madanapalle Institute of Technology&Science, Madanapalle-517325, A.P.

4. Mission10x Workshop, 12-07-2010 to 16-07-2010, MITS, Madanapalle.
5. Mobile Computing, 21st to 25th June 2010, Viswakarma Institute of Technology, Pune.
6. A National Workshop on “Advanced Processors for Embedded Systems”, 25-10-2010, MITS, Madanapalle.
7. A National Conference on “Electronic Communication Systems”, 16-03-2011, MITS, Madanapalle. An International FLI on “Embedded Computing system: From Processor Design to Code Optimization”, 20-06-2011 to 24-06-2011, MITS, Madanapalle.
8. A National Workshop on “Digital Image Processing”, 30-09-2011, MITS, Madanapalle.

MR SREENATH REDDY M:

Mr M Sreenath Reddy joined MITS in Electronics and Communication Engineering department in 2011. He completed his post-graduation from PSG Tech, Coimbatore Specialized on ‘Applied Electronics’ in 2009. He has got 7 years of teaching experience and is working as an assistant professor in MITS-ECE since 3 years. He attended 1 National Conference and 8 International Conferences. He published 1 International journal and attended 7 workshops (internal and external).

ACHIEVEMENTS OF MR. M SREENATH REDDY:

1. V. V. N. Satish Kumar, M. Sreenath Reddy, “A Better Approach For Quality Compression of Color Filter Array Data”, International Conference on Systemics, Cybernetics and Informatics 2011, pp-472-474.
2. N.V. Apparao, M. Sreenathreddy and S. Kezia, “An Efficient Approach for Image Coding Using Wavelet Based Weighted Adaptive Lifting”, International Conference on Smart Technologies for Materials, Communication, Controls, Computing and Energy, (ICST 2011), January 05-07, 2011.
3. V.R.Prasad Dudala, M.Sreenath Reddy, “Efficient Color Image Enhancement by DCT In Compression Domain”, Proceedings of Second International Conference on Signals, Systems & Automation (ICSSA-11) 24-25 January, EC Department, G H Patel College of Engineering & Technology, Gujarat, India
4. G.Govardhan Reddy, M.Sreenath Reddy, “High Speed Pattern Matching for Intrusion Detection in Network Security”, International Conference on Reliability Infocom technology and Optimization (ICRITO2011), pp-1148-1152.
5. R. Mahender, M.sreenath Reddy, “Modified Spatial Filteration for Image Denoising in Digital Image Processing”, International Conference on Systemics, Cybernetics and Informatics 2010, pp-425-428.
6. Nagaraju Panaganti, M.Sreenadh Reddy, “Video Denoising Using With Both Spatial and Temporal Filters Based On Video Codec Motion Estimation”, International Conference on Demand Computing (ICODC 2010), November 03-04, 2010.
7. S.Neelaveni, M.Sreenath Reddy, “Iris Identification Based on Feature Extraction”, Proceedings of First International Conference on Modelling, Control, Automation and Communication (ICMCAC-2010), 20th -21st December 2010. pp 114-117.
8. Attended two day Training programme on "orientation program of Engineering Faculty towards Sponsored Research" under TEQIP-II conducted by NPIU - New Delhi and SPFU Andhra Pradesh at Engineering Staff college of India, Gachibowli, Hyderabad on 04-05, October 2013.

9. Attended Three Day National Workshop on “ Virtual Instrumentation for Bio-medical Signal Processing and Systems” was Organized by R V COLLEGE OF ENGINEERING, (Autonomous Institution Affiliated to VTU, Belgaum), Bangalore - 560 059, Karnataka , during 01-03 April 2013.
10. A National Workshop on “Digital Image Processing”, 30-09-2011, MITS, Madanapalle.
11. Attended one Day workshop (05 October,2012) on “ Embedded Systems" organised by Pearson Education & Department of ECE, JNTU College of Engineering,Anantapur.
12. Published a paper titled “Artifacts Removing From EEG Signals by ICA Algorithms" in an Journal of Electrical and Electronics Engineering (IOSRJEEE) , ISSN: 2278-1676 Volume 2, Issue 4 (Oct. 2012), PP 11-16
13. Artifacts removing from EEG signals by ICA algorithms IOSRJEEE, Vol 2, Issue 4, OCT 2012, ISSN:2278-1676 PP:11-16

MR ARUN S:

Mr S Arun joined MITS in Electronics and Communication Engineering department in 13th July 2009. He completed his post-graduation from Amrita Vishwa Vidyapeetam Specialized on ‘VLSI Design’ in 2009. He has got 5 years of teaching experience and is working as an assistant professor in MITS-ECE since 5 years. He attended 1 International National Conference.

ACHIEVEMENTS OF MR S ARUN:

1. FPGA Implementation of Canonical Signed Digit Multiplier published by International Journal of Electronic and Communication Research. ISSN 2231-1246 Volume 3, Number 2 (2012), pp. 115-120 © Research India Publications.
2. Attended One day National workshop on Image processing and applications Organized by the School of computing Sciences & Engineering, VIT- Chennai on 13-03-2013, Wednesday.
3. Mission10x Workshop, 12-07-2010 to 16-07-2010, MITS, Madanapalle.
4. A National Workshop on “Advanced Processors for Embedded Systems”, 25-10-2010, MITS, Madanapalle.
5. A National Conference on “Electronic Communication Systems”, 16-03-2011, MITS, Madanapalle.
6. An International FLI on “Embedded Computing system: From Processor Design to Code Optimization”, 20-06-2011 to 24-06-2011, MITS, Madanapalle.
7. International Conference on Frontiers of Computer Science, 7th – 9th Aug 2011, Atria Institute of Technology, Bangalore.
8. A National Workshop on “Digital Image Processing”, 30-09-2011, MITS, Madanapalle.

MS C K HEMANTHA LAKSHMI:

Ms C K Hemantha Lakshmi joined MITS in Electronics and Communication Engineering department in July 2011. She completed her post-graduation from J.N.T.U.A Specialized on ‘VLSI System Design’ in 2010. She has got 5 years of teaching experience and is working as an assistant professor in MITS-ECE since 3 years. She attended 1 International Conference. She published 1 International journal along with attended 10 workshops (internal and external).

ACHIEVEMENTS OF MS C K HEMANTHA LAKSHMI:

1. A Qualitative Approach to Design Multi Channel UART Using FPGA and FIFO Technologies published by Global Journal of Researches in Engineering Volume XII Issue vVv II Version I© 2012 Global Journals Inc. (US) July 2012.
2. Basic Simulation Lab, 8th to 9th May 2010, Sree Vidyaniketan Engineering College, Tirupati.
3. A National Workshop on “Advanced Processors for Embedded Systems”, 25-10-2010, MITS, Madanapalle.
4. A National Conference on “Electronic Communication Systems”, 16-03-2011, MITS, Madanapalle.
5. An International FLI on “Embedded Computing system: From Processor Design to Code Optimization”, 20-06-2011 to 24-06-2011, MITS, Madanapalle.
6. A National Workshop on “Digital Image Processing”, 30-09-2011, MITS, Madanapalle.
7. A Qualitative Approach to Design Multi Channel UART Using FPGA and FIFO Technologies Global Journal of Researches in Engineering Volume XII Issue vVv II Version I© 2012 Global Journals Inc. (US) July 2012

MS G.R.HEMANTHA:

Mrs G.R.Hemantha joined MITS in Electronics and Communication Engineering department in 2010. She completed his post-graduation from J.N.T.U.A Specialized on ‘Digital Electronics & Communication Systems’ in 2010. She has got 4 years of teaching experience and is working as an assistant professor in MITS-ECE since 4 years. She published 1 International journal along with attended 7 workshops (internal and external).

ACHIEVEMENTS OF MS G.R.HEMANTHA:

1. GR Hemantha, Hiding data in images by LSB substitution using double polynomial, National conference on Emerging trends in electronics and communication engineering, SJBIT, Bangalore, 8 May 2010.
2. Attended Pedagogy Training Module – I, conducted by Education and Educational Management Department, NITTTR, Chandigarh from 28/10/2013 to 01/11/2013.
3. A National Workshop on “Advanced Processors for Embedded Systems”, 25-10-2010, MITS, Madanapalle.
4. A National Conference on “Electronic Communication Systems”, 16-03-2011, MITS, Madanapalle.
5. An International FLI on “Embedded Computing system: From Processor Design to Code Optimization”, 20-06-2011 to 24-06-2011, MITS, Madanapalle.
6. A National Workshop on “Digital Image Processing”, 30-09-2011, MITS, Madanapalle.

MR J.T.PRAMOD:

Mr J.T.Pramod joined MITS in Electronics and Communication Engineering department in 2010. He completed his post-graduation from J.N.T.U.A Specialized on ‘DECS’ in 2010. He has got 4 years of teaching experience and is working as an assistant professor in MITS-ECE since 4 years. He attended 2 National Conference. He published 1 International journal and attended 5 workshops (internal and external).

ACHIEVEMENTS OF MR. J.T.PRAMOD:

1. J T Pramod, Format variability for vowels using AR Model of vocal tract, National conference on Emerging trends in electronics and communication engineering, SJBIT, Bangalore, 8 May 2010.
2. Fingerprint Recognition Using Gabor Filter And Frequency Domain Filtering published by IOSR Journal of Electronics and Communication Engineering (IOSRJECE) ISSN : 2278-2834 Volume 2, Issue 6 (Sep-Oct 2012), PP 17-21, www.iosrjournals.org
3. Mr. J. T. Pramod attended One day National workshop on Image processing and applications Organized by the School of computing Sciences & Engineering, VIT-Chennai on 13-03-2013, Wednesday.
4. A National Workshop on “Advanced Processors for Embedded Systems”, 25-10-2010, MITS, Madanapalle.
5. An International FLI on “Embedded Computing system: From Processor Design to Code Optimization”, 20-06-2011 to 24-06-2011, MITS, Madanapalle.
6. “Globally Relevant Electrical and Computer Technology Curriculum”, 16-08-2011 to 17-08-2011, MITS, Madanapalle
7. A National Workshop on “Digital Image Processing”, 30-09-2011, MITS, Madanapalle.
8. J T Pramod, Sreedhar Rajput, “A Survey on Copy Move Forgery Detection Techniques”, National conference on Advanced Communication Systems & Applications, ISBN (978-93-82829-39-3), MITS, Madanapalle, 25th & 26th June 2013, PP 112-116.
9. Mr. J. T. Pramod attended training program in NSDC STAR Telecom/ retail sector (Vocational Skill Training with Industry Connect – TSSC) from Engineering Staff College of India, Hyderabad on 12 Feb 2014

MR P R RATNA RAJU K:

Mr P R Ratna Raju K joined MITS in Electronics and Communication Engineering department in 2010. He completed his post-graduation from NIT, Surat Specialized on ‘Communication Systems’ in 2011. He has got 7 years of teaching experience and is working as an assistant professor in MITS-ECE since 4 years. He attended 3 National Conferences. He attended 6 workshop (internal and external).

ACHIEVEMENTS OF MR P R RATNA RAJU K:

1. A National Conference on “Electronic Communication Systems”, 16-03-2011, MITS, Madanapalle.
2. An International FLI on “Embedded Computing system: From Processor Design to Code Optimization”, 20-06-2011 to 24-06-2011, MITS, Madanapalle.
3. A National Workshop on “Digital Image Processing”, 30-09-2011, MITS, Madanapalle.
4. Mr. P. R. Ratna Raju K, Mr. D. Balakrishna Reddy & Mr. D. Girish Kumar attended two day National workshop on "Speech Processing Research Trends & Applications" held during February 15-16, 2013 organized by Department of ECE, V.R. Siddhartha Engg College Vijayawada, under TEQIP-II.

MR. M VENKATA SRINU:

Mr. M Venkat Srinu joined MITS in Electronics and Communication Engineering department in June 2011. He completed his post-graduation from NIT Calicut Specialized on 'Signal Processing' in 2011. He has got 3 years of teaching experience and is working as an assistant professor in MITS-ECE Since 3 years. He attended 1 National Conference and 1 International Conferences. He published 1 International journal along with attended 4 workshops (internal and external).

ACHIEVEMENTS OF MR. M VENKATA SRINU:

1. Qualitative evaluation of enhancement methods for analysis of acute leukemia images, IJEST, vol.3 No.8 August 2011.
2. Color Image Enhancement Using Particle Swarm Optimization published by IRACST–Engineering Science and Technology: An International Journal (ESTIJ), ISSN: 2250-3498, Vol.2, No. 3, June 2012
3. Low –resolution satellite image enhancement using DT-CWT & SVD published by IJARECE, VOL 1,ISSUE 4, ISSN: 2278-909X, OCT 2012, PP:40-45.
4. A Modified SVD-DCT Method for Enhancement of Low Contrast Satellite Images published by International Journal Of Computational Engineering Research Vol. 2 Issue, Issn 2250-3005(online), Sept 2012.
5. Mr. M. Venkata Srinu attended five day training program at Chandigarh from 16-12-13 to 20-12-13. The training is called Pedagogical Training (Module-I) and was conducted by Department of Education and Educational Management from National Institute of Technical Teachers' Training and Research(NITTTR), Chandigarh.
6. Mr. M. Venkata Srinu attended two Day Workshop on "ACADEMIC ACTIVITIES &MANAGEMENT INFORMATION SYSTEMS [MIS]" "Under TEQIP-II conducted by NPIU - New Delhi and SPFU Andhra Pradesh at University College of engineering, Osmania University, Hyderabad on 13-14 May 2013.
7. Attended two day workshop on Industry Academia Collaboration organized by AICTE & CII at Central Leather Research Institute, Adayar, Chennai on 24th & 25th May 2013.
8. A National Workshop on "Digital Image Processing", 30-09-2011, MITS, Madanapalle.
9. Attended one Day workshop (05 October,2012) on " Embedded Systems" organized by Pearson Education & Department of ECE JNTU College of Engineering, Anantapur.
10. Attended Two day workshop on "Mathematical Methods for Image Processing and Engineering Applications" on 5th to 6th March 2013

MR. R. RAVINDRAIAH:

Mr R Ravindraiah joined MITS in Electronics and Communication Engineering department in 2011. He completed his post-graduation from J.N.T.USpecialized on 'Digital Electronics & Communication Systems' in 2011. He has got 7 years of teaching experience and is working as an assistant professor in MITS-ECE since 3 years. He attended 2 National Conferences and 6 International Conferences. He published 5 International journals along with attended 6 workshops (internal and external).

ACHIEVEMENTS OF MR. R. RAVINDRAIAH:

1. Qualitative evaluation of enhancement methods for analysis of acute leukemia images IJEST, vol.3 No.8 August 2011, ISSN: 0975-5462.
2. Segmentation of Electron Micrograph Images for Qualitative Analysis of Diabetic Atherosclerosis, IJAER Volume 6, Number 17 (2011), PP.2061-2068.
3. Mr. R. Ravindraiah attended One day National workshop on Image processing and applications Organized by the School of computing Sciences & Engineering, VIT-Chennai on 13-03-2013, Wednesday.
4. Mr. R. Ravindraiah participated in a Conference/ Workshop “International Conference on Digestive Disorders & Nursing Symposium, 23-24, February 2013, Organized by IAPEN - Bangalore Regional Center and IAPEN - Tirupati Regional Center.
5. An International FLI on “Embedded Computing system: From Processor Design to Code Optimization”, 20-06-2011 to 24-06-2011, MITS, Madanapalle.
6. MATLAB and Simulink for Engineering Education, 06-09-2011, MathWorks India
7. A National Workshop on “Digital Image Processing”, 30-09-2011, MITS, Madanapalle.

MR. LASHOK:

Mr L.Ashok joined MITS in Electronics and Communication Engineering department in Dec 2011. He completed his post-graduation from NIT, Calicut Specialized on ‘Tele Communications’ in 2011. He has got 3 years of teaching experience and is working as an assistant professor in MITS-ECE since 3 years. He attended 1 International Conference. He attended 4 workshops (internal and external).

ACHIEVEMENTS OF MR. LASHOK:

1. Attended Two day workshop on “Mathematical Methods for Image Processing and Engineering Applications” on 5th to 6th March 2013

MR. P. SRAVAN KUMAR:

Mr P. Sravan Kumar joined MITS in Electronics and Communication Engineering department in 2011. He completed his post-graduation from NIT Calicut, specialized on ‘Tele Communications’ in 2011. He has got 2 years of teaching experience and is working as an assistant professor in MITS-ECE since 2 years. He attended 5 workshops (internal and external).

ACHIEVEMENTS OF MR. P. SRAVAN KUMAR:

1. Attended Two day workshop on “Mathematical Methods for Image Processing and Engineering Applications” on 5th to 6th March 2013

MS G NAGASWETHA:

Ms G Nagaswetha joined MITS in Electronics and Communication Engineering department in 2011. She completed her post-graduation from AITS, Rajampet Specialized on ‘Embedded Systems’ in 2010. She has got 8 years of teaching experience and is working as an assistant

Professor in MITS-ECE since 3 years. She attended 1 National Conference. She attended 5 workshops (internal and external).

ACHIEVEMENTS OF MS G NAGASWETHA:

1. Had got a reward of 3000/- in a national conference ABHISAARA 2K9 for a paper “ Stock clock and weather forecasting based on Zigbee Network” in the year 2009
2. Attended a workshop in AITS , Rajampet “WESLA” in the year 2006
3. Attended a workshop in MITS, Madanapalle “ Digital IC applications” in year 2012.
4. Attended a workshop in GRIET, Hyderabad “ VSIM applications in Embedded Systems” in year 2013
5. Attended a workshop in MITS, Madanapalle “ Wireless Communications” in year 2013
6. Attended a workshop in ESCI, Hyderabad “Formulation of Research & Development Initiatives for women scientists and Technologists” in the year 2014.

MR SATISH KUMAR V:

Mr Satish Kumar V joined MITS in Electronics and Communication Engineering department in 2012. He completed his post-graduation from NIT, Calicut Specialized on ‘Signal Processing’ in 2012. He has got 2 years of teaching experience and is working as an assistant professor in MITS-ECE since 2 years. He attended 2 National Conferences and published 1 international journal.

ACHIEVEMENTS OF MR SATISH KUMAR V:

2. Presented and published a paper titled “Reducing the complexity of FIR filters by using prototype and masking filters splitting with FRM” in two days National conference on Advanced Communication systems & Applications Proceedings – ISBN 978-93-82829-39-3 organized by Dept of ECE, Madanapalle Institute of Technology & Science on 25th & 26th June 2013.
3. Presented and published a paper titled “Poisson Image Denoising By Using Modified Anscombe Transformation” in two days National conference on Advanced Communication systems & Applications Proceedings – ISBN 978-93-82829-39-3 organized by Dept of ECE, Madanapalle Institute of Technology & Science on 25th & 26th June 2013.
4. Published the paper titled “*Design of Low Power and Low Complexity Multiplier-less Reconfigurable Non-uniform Channel filters using Genetic Algorithm*” in Global Journal of researches in engineering Electrical and electronics engineering, Volume 12 Issue 6 Version 1.0 May 2012.
5. Attended three days international workshop on “ Wireless Networks” under TEQIP-II as part of the 2013 Indo- US Engineering Faculty Leadership Institute from 18th to 20th June 2013 at Madanapalle Institute of Technology & Science, Madanapalle, AP, India
6. Participated in Two-week workshop on “Analog Electronics” under National Mission on Education through ICT (MHRD, Govt. of INDIA) on 4th to 14th June 2013 conducted by IIT Kharagpur, INDIA.
7. Attended Two day workshop on “Mathematical Methods for Image Processing and Engineering Applications” on 5th to 6th March 2013
8. Participated in one day National Level Workshop on “NPTEL AWARENESS” on 4th January 2013.

9. Attended one day workshop on “Embedded Systems” on 5th October 2012 organized by Dept of ECE, JNTUA College of Engineering (Autonomous), Anantapur-AP.
10. Attended one week International workshop on “ Data Communications IC Design” under TEQIP-II as part of the 2012 Indo- US Engineering Faculty Leadership Institute from 25th to 29th June 2012 at Madanapalle Institute of Technology & Science, Madanapalle, AP, India
11. Participated one day National level Technical Seminar on “Software Defined Radio- The Future Radio” on 23rd February 2012 organized by Dept of ECE, VIT-Chennai, INDIA.

MR. BALAKRISHNA REDDY D:

Mr Balakrishna Reddy D joined MITS in Electronics and Communication Engineering department in 2012. He completed his post-graduation from NIT, Calicut Specialized on ‘Signal Processing’ in 2010. He has got 2 years of teaching experience and is working as an assistant professor in MITS-ECE since 2 years. He attended 2 workshops (internal and external).

ACHIEVEMENTS OF MR. BALAKRISHNA REDDY D:

1. Mr. P. R. Ratna Raju K, Mr. D. Balakrishna Reddy & Mr. D. Girish Kumar attended two day National workshop on "Speech Processing Research Trends & Applications" held during February 15-16, 2013 organized by Department of ECE, V.R. Siddhartha Engg College Vijayawada, under TEQIP-II.

MR. B.VAMSI KRISHNA:

Mr. B. Vamsi Krishna joined MITS in Electronics and Communication Engineering department in 2012. He completed his post-graduation from GPREC, Kurnool Specialized on ‘Communications and Signal processing’ in 2011. He had 3 years of teaching experience and is working as an assistant professor in MITS-ECE since 2 years. He attended 1 National Conference. He published 3 International journals and attended 3 workshops (internal and external).

ACHIEVEMENTS OF MR. B. VAMSI KRISHNA:

1. A Game Theoretic Analysis of Fixed Channel Allocation for Multiple Radios in Multihop Wireless Networks IJCST, Vol. 3, Issue 2, April - Jun ISSN : 09768491 (Online) | ISSN: 22294333 (Print) 2012.
2. Reducing the jitter noise power by oversampling in High speed OFDM system, International Journal of Engineering and Science Invention (IJESI) Volume 2 Issue 3 ISSN (Online): 2319 – 6734, ISSN (Print): 2319 – 6726 March 2013 PP: 42-46.
3. Participated in a two day National workshop on "Simulation of Wireless Networks" Jointly organised by JNTUH, College of Engineering, Hyderabad and Nihon communications pvt Ltd, Bangalore Under TEQIP during 15th and 16th April 2013.
4. Participated in a two day national workshop on "Current Trends in Wireless Communications” Organized by School of Electronics Vignan University, Vadlamudi, Guntur Dist, during 8th- 9th January 2013.
5. A Novel Nash equilibrium technique for Channel allocation in Multihop wireless

networks, Advanced Communication systems & Applications, MITS, Madanapalle, 25 to 26 June 2013, ISBN 978-93-82829-39-3

MR. SREENIVASULU U:

Mr Sreenivasulu U pursuing PhD from JNTU Hyderabad in WIRE LESS COMMUNICATION specialization. He joined MITS in Electronics and Communication Engineering department in 2012. He has got 6 years of teaching experience and is working as an assistant professor in MITS-ECE since 2 years. He attended 1 National Conference. He published 1 national journal and attended 6 workshops (internal and external).

ACHIEVEMENTS OF MR. SREENIVASULU U:

1. Mr. U Sreenivasulu attended Two day workshop on “Xilinx dsp design flow and implementation of image and signal processing applications using xilinx FPGA’s (sponsored by APSCHE) & Conducted by Department of Electronics & Communication engineering JNTUA college of Engineering (Autonomous) Ananthapuramu, Andhra Pradesh held during 24th -25th January 2014.
2. Attended Two day workshop on “Mathematical Methods for Image Processing and Engineering Applications” on 5th to 6th March 2013

MR. GIRISH KUMAR D:

Mr Girish kumar D joined MITS in Electronics and Communication Engineering department in 2013. He completed his post-graduation from VR Siddhartha Engg Clg, Vijayawada Specialized on ‘Communications and Signal processing’ in 2012. He has got 1 year of teaching experience and is working as an assistant professor in MITS-ECE since 1 year.

ACHIEVEMENTS OF MR. GIRISH KUMAR D:

1. He attended a National Level Conference on “ADVANCED SYSTEM AND APPLICATIONS”.
2. He attended work shop on “speech processing”.
3. He published a journal on “A novel image processing technique for counting the number of trees in a satellite image” in 2012.
4. Mr. P. R. Ratna Raju K, Mr. D. Balakrishna Reddy & Mr. D. Girish Kumar attended two day National workshop on "Speech Processing Research Trends & Applications" held during February 15-16, 2013 organized by Department of ECE, V.R. Siddhartha Engg College Vijayawada, under TEQIP-II.

MR. SAMBA SIVA RAO G:

Mr Samba siva rao G joined MITS in Electronics and Communication Engineering department in 2013. He completed his post-graduation from College of Engg, Trivandrum Specialized on ‘Micro Wave &TV Engineering’ in 2009. He has got 5 years of teaching experience and is working as an assistant professor in MITS-ECE since 1 year. He attended 3 International Conferences. He published 2 International journals and attended 5 workshops (internal and external).

ACHIEVEMENTS OF MR. SAMBA SIVA RAO G:

1. "A Nanopower Temperature Insensitive Programmable FGMOS Current Reference" *International Conference on New Horizons in Green Energy with Smart Communication Systems -2013 (ICGESCS-2013)* Feb1-2 2013, Tamilnadu
2. "Current Modeling of independent double gate FINFET" *IJERT JOURNAL* Vol. 2 Issue 1, January- 2013 ISSN: 2278-0181
3. "A Novel FGMOS Voltage Reference with Temperature and Power Supply Compensation" *IJERT JOURNAL* Vol. 1 Issue 9, November- 2012 ISSN: 2278-0181
4. "A Novel FGMOS Voltage Reference With Temperature and Power Supply Compensation" *IEEE-International conference on Advanced Communication Control and Computing Technologies-2012 (ICACCCT-2012)* page(s) 394-397, Aug 24-25 2012, Tamilnadu
5. "Low temperature Coefficient of CMOS Voltage reference Based on Subthreshold Operation" *IEEE-International conference on Advances on Science Engineering and Management-2012 (ICASEM-2012)* Page(s):238 – 241, March 30-31 2012, Tamilnadu

MS R D NAGA MOUNIKA:

Ms Naga mounika joined MITS in Electronics and Communication Engineering department in 2013. She completed her post-graduation from Adhiyamaan college of Engineering Specialized on 'communication systems' in 2012. She has got 1 year of teaching experience and is working as an assistant professor in MITS-ECE since 1 year. She attended 1 National Conference and 2 International Conferences.

ACHIEVEMENTS OF MS R D NAGA MOUNIKA:

1. Attended a National Conference on "*Integrated optic micro ring resonator design for DWDM applications*" at Adhiyamaan College of engineering during 21st and 22nd March 2012.
2. Attended an International Conference on "*Comparison of STIP and RIB waveguide couplers using finite difference method*" at MARIA College of engineering & technology, ATTOOR, Kanyakumari during 8th and 9th March 2012.
3. Attended an International Conference on "*Numerical analysis of integrated optic hemoglobin sensor*" at MS Ramaiah Institute of Technology, Bangalore during 8th to 10th June 2012.

ACHIEVEMENTS OF ECE FACULTY DURING 2013-14:

1. Dr A R Reddy attended 7 days training program, on “Training on Management Capacity Enhancement (Batch 7) for Administrators” organized by IIM Indore with NPIU during January 12 - 18, 2014, under TEQIP II program.
2. Dr S A K Jilani attended India’s Flagship Nanotech Event - 3 day conference on 6th Bangalore India Nano organized by the Department of IT, Biotechnology and Science & Technology, Government of Karnataka under the able guidance of the Vision Group on Nanotechnology from 4th to 6th December 2013.
3. Dr D Asha Devi Presented paper entitled “A Cloud based Security for Intelligent System” at 3rd National Conference on computer networks and information security - 2014 Sponsored by AICTE and organized by Department of IT, Vasavi Engineering College, Hyderabad on 4th & 5th March 2014.
4. Dr D Asha Devi Presented a paper on “A Study on the Agricultural Development of Anantapur Anantha Water Grid (Anantha JalaValayam)” to the first two day national conference on sustainable water resource management (Solutions, technologies & opportunities) – organized by Department of Civil engineering, Maharaj vijayaram Gajapathi Raj College of engineering, Vijayanagaram sponsored by AICTE on 21st & 22nd February 2014.
5. Dr D Asha Devi attended International Conference on Competency Building Strategies in Business and Technology for Sustainable Development and Presented a paper titled “An authentication frame work for ubiquitous computing environment” Organized by Sri Ganesh School of Business Management,Attur Main Road, Mettupatti, Valapaddy (Tk),Salem-Dt, Tamil Nadu, INDIA held on 22 February 2013.
6. Dr D Asha Devi Presented a paper titled ”A Study on Security challenges for ubiquitous computing using role based access control system” in a National conference on Cryptography and network security” Organized by Department of CSE, SVS Group of Institutions, Bheemaram, Hanamkonda, on 22-03-2013 & 23-03-2013.
7. Dr D Asha Devi attended "AICTE Sponsored Faculty Development Program (FDP) on “VLSI DESIGN” organized by VNR Vignana Jyothi Institute of Engineering & Technology, Hyderabad from 20-05-2013 to 01-06-2013 (10AM to 5PM).About 80 participants were benefited by the training programme.
8. Dr D Asha Devi Appointed as Reviewer for INTERNATIONAL JOURNAL FOR ADVANCED ENGINEERING & GLOBAL TECHNOLOGY (IAEGT).
9. Dr D Asha Devi Attended India’s Flagship Nanotech Event - 3 day conference on 6th Bangalore India Nano organized by the Department of IT, Biotechnology and Science & Technology, Government of Karnataka under the able guidance of the Vision Group on Nanotechnology from 4th to 6th December 2013.
10. Dr. J L Mazher Iqbal Participated in 2 days workshop on, “Wireless Communications Using MATLAB and Simulink” Organized by TIFAC - CORE in AUTOMOTIVE INFOTRONICS (Sponsored by Department of Science and Technology, Govt. of India) VIT Vellore held during 21/02/2014 to 22/02/2014.
11. Dr. J L Mazher Iqbal Published a paper titled “Public Safety Communication using Relay Node in LTE-Advanced Technology” in an International Journal of Engineering

Research & Technology (IJERT), ISSN: 2278-0181, ISO 3297:2007, Vol. 2 Issue 11, November – 2013, pp: 1767-1772.

12. Mr. Mahesh Published a paper titled Feature Based Image Mosaic Using Steerable Filters and Harris Corner Detector in an International Journal of Image, Graphics and Signal Processing (IJIGSP) ISSN: 2074-9074(Print), ISSN: 2074-9082 (Online)" Pp-09-15, DOI: 10.5815/ijigsp, Volume 5, Number 6, May 2013 .
13. Mr. Mahesh Presented a paper titled "Automatic Image Mosaic system Using Steerable Harris corner Detector " in an international Conference on Machine Vision and image Processing (MVIP12), Organized by the Department of Instrumentation and Control Engineering, PSG College of Technology,Coimbatore-6411004, Tamil Nadu, India.
14. Mr. B. Sukumar attended Pedagogical training at Engineering Staff College of India (ESCI), Hyderabad during 20-01-2014 to 25-01-2014.
15. S Arun attended One day National workshop on Image processing and applications Organized by the School of computing Sciences & Engineering, VIT- Chennai on 13-03-2013, Wednesday.
16. G R Hemantha attended Pedagogy Training Module – I, conducted by Education and Educational Management Department, NITTTR, Chandigarh from 28/10/2013 to 01/11/2013.
17. Mr. J. T. Pramod attended training program in NSDC STAR Telecom/ retail sector (Vocational Skill Training with Industry Connect – TSSC) from Engineering Staff College of India, Hyderabad on 12 Feb 2014
18. Mr. J. T. Pramod attended One day National workshop on Image processing and applications Organized by the School of computing Sciences & Engineering, VIT-Chennai on 13-03-2013, Wednesday.
19. Mr. R. Ravindraiah attended One day National workshop on Image processing and applications Organized by the School of computing Sciences & Engineering, VIT-Chennai on 13-03-2013, Wednesday.
20. Mr. R. Ravindraiah participated in a Conference/ Workshop "International Conference on Digestive Disorders & Nursing Symposium, 23-24, February 2013, Organized by IAPEN - Bangalore Regional Center and IAPEN - Tirupati Regional Center.
21. Mr. M. Venkata Srinu attended five day training program at Chandigarh from 16-12-13 to 20-12-13. The training is called Pedagogical Training (Module-I) and was conducted by Department of Education and Educational Management from National Institute of Technical Teachers' Training and Research(NITTTR), Chandigarh.
22. Mr. M. Venkata Srinu attended two Day Workshop on "ACADEMIC ACTIVITIES &MANAGEMENT INFORMATION SYSTEMS [MIS] "Under TEQIP-II conducted by NPIU - New Delhi and SPFU Andhra Pradesh at University College of engineering, Osmania University, Hyderabad on 13-14 May 2013.
23. Attended two day workshop on Industry Academia Collaboration organized by AICTE & CII at Central Leather Research Institute, Adayar, Chennai on 24th & 25th May 2013.
24. Mr. M. Jagadeesh Babu attended Two day workshop on "Xilinx dsp design flow and implementation of image and signal processing applications using xilinx FPGA's (sponsored by APSICHE) & Conducted by Department of Electronics & Communication engineering JNTUA college of Engineering (Autonomous) Ananthapuramu, Andhra

Pradesh held during 24th -25th January 2014.

25. Mr. M. Jagadeesh Babu attended India's Flagship Nanotech Event - 3 day conference on 6th Bangalore India Nano organized by the Department of IT, Biotechnology and Science & Technology, Government of Karnataka under the able guidance of the Vision Group on Nanotechnology from 4th to 6th December 2013.
26. Mr. M. Sreenath Reddy attended two day Training programme on "orientation program of Engineering Faculty towards Sponsored Research" under TEQIP-II conducted by NPIU - New Delhi and SPFU Andhra Pradesh at Engineering Staff college of India, Gachibowli, Hyderabad on 04-05, October 2013.
27. Mr. M. Sreenath Reddy attended Three Day National Workshop on "Virtual Instrumentation for Bio-medical Signal Processing and Systems" was Organized by R V COLLEGE OF ENGINEERING, (Autonomous Institution Affiliated to VTU, Belgaum), Bangalore - 560 059, Karnataka, during 01-03 April 2013.
28. Mr. B. Vamsi Krishna participated in a two day National workshop on "Simulation of Wireless Networks" Jointly organised by JNTUH, College of Engineering, Hyderabad and Nihon communications pvt Ltd, Bangalore Under TEQIP during 15th and 16th April 2013.
29. Mr. B. Vamsi Krishna participated in Workshop on "Current Trends in Wireless Communications" Organized by School of Electronics Vignan University, Vadlamudi, Guntur Dist, during 8th- 9th January 2013.
30. Mr. U Sreenivasulu attended Two day workshop on "Xilinx dsp design flow and implementation of image and signal processing applications using xilinx FPGA's (sponsored by APSCHE) & Conducted by Department of Electronics & Communication engineering JNTUA college of Engineering (Autonomous) Ananthapuramu, Andhra Pradesh held during 24th -25th January 2014.
31. Ms GR Hemantha, Ms C.K. Hemantha Lakshmi & Ms G. Naga Swetha, attended 3 - day National workshop on "Formulation of Research & Development Initiatives for Women Scientists and Technologists" organized by Environment Management Division of Engineering Staff College of India from 05 — 07 February, 2014 at ESCI Campus, Gachibowli, Hyderabad.
32. Mr. P. R. Ratna Raju K, Mr. D. Balakrishna Reddy & Mr. D. Girish Kumar attended two day National workshop on "Speech Processing Research Trends & Applications" held during February 15-16, 2013 organized by Department of ECE, V.R. Siddhartha Engg College Vijayawada, under TEQIP-II.
33. Mr. M Venkata Srinu, Mr. P Sravan Kumar, Mr. L Ashok, Mr. U Sreenivasulu & Mr. V Satish Kumar attended Two day workshop on Mathematical Methods for research in Image processing and Engineering applications Organized by Department of ECE at CBIT, Hyderabad. Supported by TEQIP II on 05-03-2013 & 06-03-2013.
34. Mrs. G.R.HEMANTHA, MS. C.K.HEMANTHA LAKSHMI, & Mrs. G.NAGA SWETHA attended two day workshop on "Model Based Design for Embedded Systems with VisSim" organized by Trident Techlabs in association with Visual Solutions, Inc. on 7th and 8th March 2013.

ACHIEVEMENTS BY ECE STUDENTS DURING 2013-14:

1. Mr. P. ANAND KUMAR (11691A0402) and Mr. Y. MALLIKARJUNA (11691A0452) , ECE Department Participated and presented a paper titled "INTELLIGENCE AMPLIFICATION SYSTEM FOR MILITARY OPERATIONS" in KonvergenZ'14, a National Level Technical Symposium organized by KSRM College of Engineering, Kadapa, , Andhra Pradesh held during February 11-13 2014 & secured **Third Prize** and also participated in HACK-TRIX workshop held from 10th to 13th at KSRM College of Engineering organized by Techno Matrix



1. Mr. C. Dinesh (10691A0419) and Mr. M. Kumar Raja (10691A0443) bagged **Second prize** in paper presentation "AN INTELLIGENCE AMPLIFICATION SYSTEM FOR MILITARY OPERATIONS IN URBAN TERRAIN USING AUGMENTED REALITY" at PRAYAGA 2013, a National Level Technical Paper Presentation organized Tirumala Engineering college, Jonnalagadda(P), Narasaraopet-522601, Guntur(Dist), A.P. (09-10 January 2013).



2. Mr. K. Goutham Kumar (10691A0425), Ms. S. Fouziya Zeehana (10691A0421) Presented paper on "ELECTROCULOGRAPHY GUIDED AUTONOMOUS ROBOTS" and K. Goutham Kumar (10691A0425), A. Balaji (10691A0414), A. Bramha Kumar (10691A0416), Mr. .M. Kumar Raja(10691A0443) Participated in SHORT FILM titled" HATSOFF INDIA " at VIT PRAGNA 2013, Second National Level conference organized by Vemu Institute of Technology, P. Kothakota (P), Puthalapattu (M), Chittoor Dist.-517112 A.P.



3. Mr. C. Dinesh (10691A0419) of IV B.Tech (ECE) bagged the First prize for the topic "Augmented Reality" in the paper presentation Category at National Level Symposium organized by SVCE & SVEW, Tirupati, Chittoor Dist.-517 112 , A.P. on 24-01-2013.



4. Mr. M. Kumar Raja(10691A0443) of IV B.Tech (ECE) bagged the **Second Prize** in Short Film Category for the Topic "Engineering Schemes in India" at National Level Symposium organized by SVCE & SVEW, Tirupati Chittoor Dist.-517 112 ,A.P. on 24-01-2013.

5. Ms. V. Anusha (10691A0409) and MS. M. Monisha Swarna Sree (10691a0456), ECE Department presented a Technical paper entitled " QDEO VIDEO PROCESSING TECHNOLOGY " in a conference on advances in Technological Developments (DBS ECSTASY-2013) held on - Feb 16, 2013 at DBS Institute of Technology , Kavali, Andhra Pradesh



6. Mr. S. Pratap Reddy (10691A0473) and Mr. P. Sai Jaswanth Nikhil (10691A0487), ECE Department presented a Technical paper entitled "HUMAN EMOTION-DETECTION USING BRAIN WAVE SIGNAL PROCESSING" in e-merge13, a technical-fest at JNTUA College of Engineering, Anantapur, Andhra Pradesh during February 22-23, 2013.



7. The students of IV ECE, Mr. C. Dinesh(10691A0419), Mr. M. Kumar Raja(10691A0443), Mr .A. Bramha Kumar(10691A0416), Mr. M. Kiran(10691A41), Mr. C.Naga vijayendra (10691A0459), Mr. C. Naveen Kumar Reddy (10691A0465), Mr. K. chakradhar Reddy(10691A0417),Mr. M. Janardhan Reddy(10691A0431) and P.Ahok Kumar(10691A410) were selected and participated in the events like Paper Presentation, Technical quiz and Short Film in Trishna 2k13, a Two day National Level Technical Symposium & Workshop on Graphical Computing Interfaced with Robotics organized Annamacharya Institute of Technology & Sciences, Tirupati, AP, India during February 15th-16th, 2013.





8. Ms. P.Madhavi (10691A0449) student of III ECE Department has Participated in CONCEITO- Poster Presentation held on 23-02-2013, organized by Sree Vidyanikethan Engineering College and has secured **First Prize**.



9. The students of IV ECE, Mr. C. Dinesh, Mr. B. Jagadeesh, Mr. K. Gnanatheja, Mr. S. Achyuth, Mr. D. Koti Reddy, Mr. S. Lokesh, Mr. D. Deepak, and Mr. D. Madan Mohan, attended for "TECHNOCRATS-2013" which was held at Sai sakthi Engineering College, Maharajapuram, Chittor district, AP .



10. Mr. G. Vinayaka (09691A04B7) and Mr. M. Siva (09691A0494) Participated and bagged First Prize in Project Demonstration on "Zibbed Based Conduct of Exams" held during February 25-26, 2013 organized by Daita Madhusudhana Sastry Sri Venkateswara Hindu College of Engineering, Machilipatnam Krishna District.

11. Mr.D.Hari Krishna (11695A0406), IV B.Tech. ECE Department has participated and presented paper titled "Night Vision Technology" in a National Symposium-RIPPLE 2K13, organized by Rajeev Gandhi Memorial College of Engineering & Technology, Nandyal, Kurnool (Dist) AP, on 01.03.2013 and won **Third prize**.



12. Mr. V. Amarnath (10691A0403), Mr. K. Chakradhar Reddy (10691A0417) and A. KiranKumar Reddy(10691A0439), III B.Tech, ECE Department have Participated in a Grand Quiz at Mohan Mantra 2013, a National Level Techno & Cultural fest held on Feb 22-24 2013 at Sree Vidhyanikethan Engineering College, Tirupati, Andhra Pradesh and got **Second Position**.



13. Ms. T.P.Niveditha (10691A0467) and Ms P.Nagaraja Swathi (10691A0460), ECE Department presented a Technical paper entitled "ULTRA HIGH SPEED CMOS IMAGE SENSORS" at Mohan Mantra 2013, a National Level Techno & Cultural fest held on - Feb 22,23 2013 at Sree Vidhyanikethan Engineering College, Tirupati, Andhra Pradesh.



14. Ms. M.Vijaya (12691A04F6), Mr.Mutyala Samara Simha Reddy (12691A04B7) & Ms. Gopathi Susmithasen (12691A04E1) stood **college topper's** of B.Tech I year 2013.

15. Mr.H.Balaji(10691A0415) ,IV ECE, Participated and presented a paper titled " The Revolutionary Tetwalker (An Upcoming Technology)" in a one day National level symposium 'CONTEZZA-2K13' was conducted in' Kuppam Engineering College, Kuppam, Chittor (Dt), A.P' on 15 March 2013 and secured **Second Prize** .

16. Mr.K.Goutham Kumar (10691A0425) Participated and presented a paper titled " Trajectory tracking of a autonomous robot using eye movement coordination (electrooculography) in the departments of ME and EEE and also presented a paper titled " Intelligence amplification system for military operations in urban terrain" using augmented reality in the department of ECE in TECHWAR-2K13, a National Level Technical Symposium which was organized by GLOBAL COLLEGE OF

*ENGINEERING & TECHNOLOGY, Chinnamachupalli (V), Chennur Road, Kadapa, Y.S.R (Dist) -516162, A.P. and secured two second prizes and one **third prize**.*

- 17. Mr.C.Anil Kumar Reddy (10691A0406) and Mr.K.Goutham Kumar (10691A0425) Participated in Paper Presentation & Poster Design in CONTEZZA-2K13, a National Level Technical Symposium Conducted by Department Of Electrical And Electronic Engineering, KUPPAM ENGINEERING COLLEGE on 15th March 2013 and secured **First Prize** in Paper Presentation and Second Prize in Poster design.*
- 18. S.Kiran Kumar (10691A0440), and S. Pratap Reddy (10691A0473) Participated in Paper Presentation in TECHNOGV 2K13, a two day National level symposium Organized by "Golden Valley Integrated Campus Madanapalle Andhra Pradesh on 08 and 09 of March 2013 and secured **Third Prize**.*
- 19. K.G.Nandini (10691A0461), K.Vaishnavi(10691A04A7) Participated in Posteromine ELECTRONICA 2K13', a two day National level symposium Organized by Department of Electronics & Communication Engineering, G. Pulla Reddy Engineering College, Kurnool, AndhraPradesh on 9-MAR-2013 and secured **Second Prize** for the poster" GI FI(WIRELESS COMMUNICATION).*
- 20. G.Bhavya Reddy(09691A0414), M.K.Fayaz (09691A0438) and P.Khaja (09691A0439) Participated in Project EXPO 'AVISHKARA 2K13', a two day National level symposium Organized by ' Sri Vishveshwaiah Institute of Science and Technology, Madanapalle' on 15th - 16th March 2013 and secured **third Prize** for the project" STEREO VISION BASED DISPARITY MEASUREMENT.*
- 21. 19. Mr.C.Anil Kumar Reddy(10691A0406), D.Koti Reddy (10691A0472) and Mr.S.Pratap Reddy(10691A0473) Participated in Technoquiz in TECHNOGV 2K13, a two day National level symposium Organized by ' Golden Valley Integrated Campus Madanapalle Andhra Pradesh on 08 and 09 of March 2013 and secured **First Prize**.*

STUDENTS PLACED FOR VARIOUS COMPANIES

ARICENT TECHNOLOGIES:



U. NAVEEN RAVI
KUMAR



T.P. NIVEDITHA



R. MANJULA



Md. RAYAN SHAIK



P. VASAVI BHAVANI



S. SOPIYA



K. VAISHNAVI



V. ANUSHA



C. DINESH

TECH MAHINDRA:



D. DEEPAK



C. SREEKALA



A. RAJESH

NTT DATA:



C. NAGA
VIJAYENDRA



G. JAYA PRAKASH



K. SREENIVASA
REDDY

ARTICLES BY STUDENTS

Gallium – A Rare Metal with Interesting Properties:

A truly peculiar metal with an ultra-low melting point of just 29.76 °C (85.57 °F) and the ability to attack most other metals by diffusing into their metal lattice .In 1871, existence of gallium was first predicted by Russian chemist Dmitri Mendeleev, who named it "eka-aluminium" and it was discovered spectroscopically by French chemist Paul Emile Lecoq de Boisbaudran



It easily alloys with many metals, and is used in small quantities as a plutonium-gallium alloy in the plutonium cores of nuclear bombs, to help stabilize the plutonium crystal structure. Gallium arsenide, the primary use of gallium, is used in microwave circuitry and infrared applications.

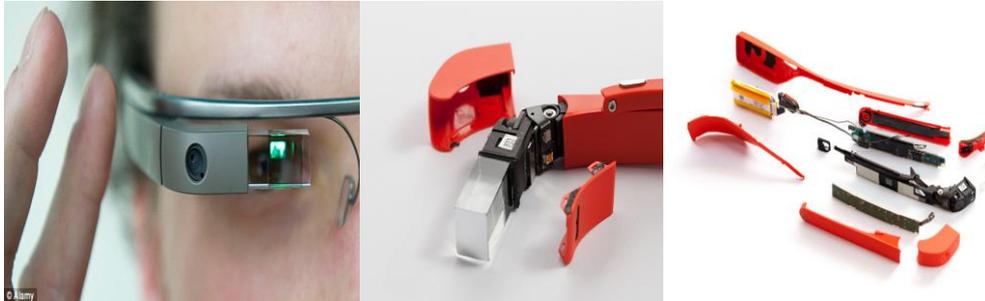
Fab Industries:



T Praveen Kumar
III year, B Sec

Google Glasses and Pebble Watches for Investors:

Fidelity Investments is releasing today an app that allows you to get alerts and stock prices on a Pebble smart watch which follows a similar software release last fall that displays stock data on a Google Glass appliance. Where you can now keep up to date on your portfolio with a glance at your wrist. The number of trades placed with mobile devices was up 63% last year and spends \$2.5 billion a year on technology.



G Deavi Lalitha
II year, A Sec

Fabrication Units

(A boon to electronic development in INDIA...)

In the past no one might have expected even in their dreams that an element in the periodic table having atomic number 14 might have such a devastating future in the present electronic era. The element is nothing but silicon which plays a vital role in the development of integrated circuits as such a pacemaker. The main element in fabrication industries is silicon material which is abundant in our surroundings, then why should not India enter into the industry of fabrication???

IMPORTANCE OF FABS IN INDIA:

- ✚ India is strong in semiconductor design - according to The Department of Electronics and IT, nearly 2000 chips are being designed every year in India and more than 20, 000 engineers are working on various aspects of chip design and verification
- ✚ According to the recent stats India consumes close to \$7 billion worth of semiconductor products every year. By 2020, this consumption can rise to \$55 billion. "With the location of fabs in India, the country could achieve a degree of self-sufficiency in electronics, and partially reduce the very high supply chain risks that India is exposed to, without an alternate source for procurement".
- ✚ Demand for electronic goods in India is expected to reach \$11 billion by 2014, up 83 percent from nearly \$6 billion in 2010. Even more explosive growth is seen over the next decade as more of the country's 1.21 billion people get access to the Internet thanks to Smartphone's, laptops, and tablet computers becoming more affordable.
- ✚ Some Indian companies are already actively seeking opportunities to set up fabs in the country with the support of foreign chip vendors. Hindustan Semiconductor Manufacturing Co., for instance as Infineon Technologies. Router and set-top box vendor SemIndia is said to

be seeking to partner with Advanced Micro Devices to set up manufacturing operations in India.

- ✦ India's semiconductor imports were \$8.2 billion in 2012, according to Gartner. The demand is growing at around 20 percent a year, according to the Department of Electronics and Information Technology.
- ✦ By having fabs India's average of gross domestic product increases by 3.2% for every year. The Indian government estimates that the creation of a chip making industry within India will help create 3 crore direct and indirect jobs by 2020.

E Santhosh
III year, B Sec